



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Wolfgang Roethig
Assignee: NEC Electronics America, Inc
Title: SYSTEM AND METHOD FOR CALCULATING EFFECTIVE
CAPACITANCE FOR TIMING ANALYSIS
Serial No.: 10/662,540 Filing Date: September 15, 2003
Confirmation No.: 2264 Date Allowed: April 7, 2005
Examiner: Paul Dinh Group Art Unit: 2825
Docket No.: NEC0250US

Austin, Texas
May 3, 2005

MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
P. O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit seven (7) sheets of formal drawings, consisting of Figures 1, 2, 3; 4; 5A, 5B; 6; 7; 8, 9; 10 in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (512) 439-5087.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to MAIL STOP ISSUE FEE, Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on May 3, 2005.

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5-3-2005
Date of Signature

Respectfully submitted,

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